CSE211: Compiler Design Oct. 20, 2021

- **Topic**: Local optimizations (Local Value Numbering)
- Questions:
 - How can you optimize arithmetic expressions?

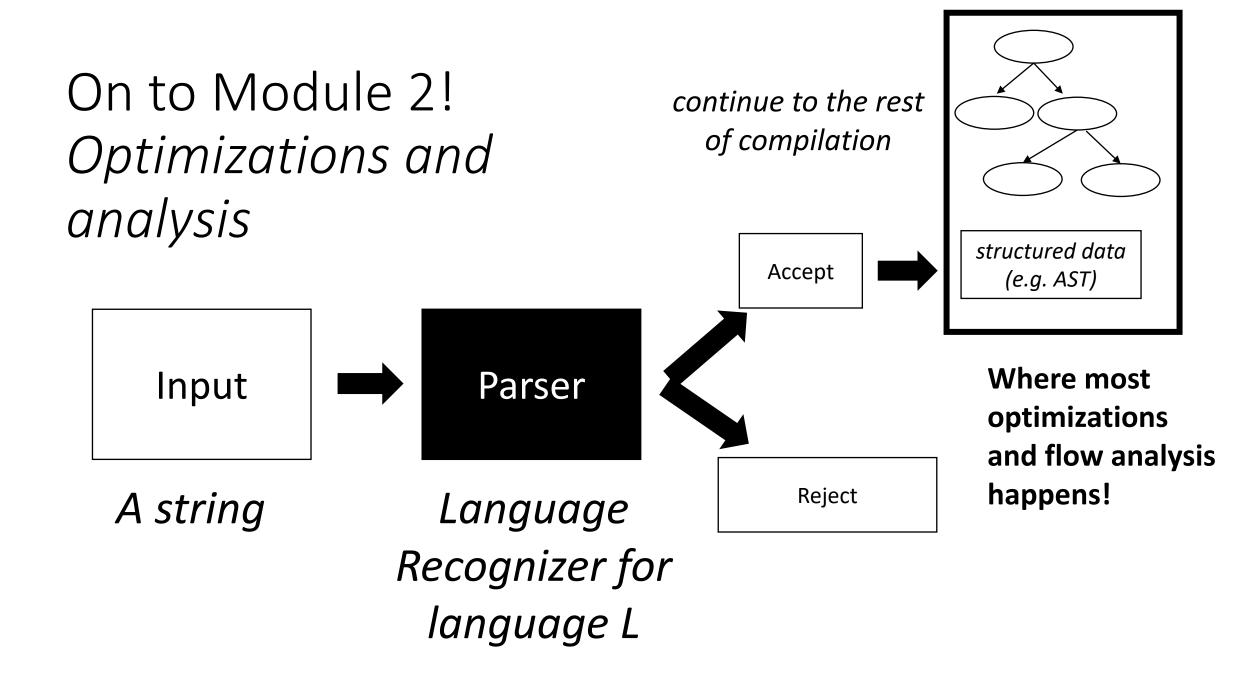
Announcements

- Homework 1 is out
 - Due on the 25th
 - No extensions
- Get your paper reading approved by me by Monday
 - No extensions, 5% of your grade
- Ask questions on Piazza if you have questions

Announcements

- IF I AM FEELING WELL ENOUGH: I will be gone Monday and Wednesday next week to attend a khronos group meeting.
 - The schedule is still in flux:
 - either I will hold class synchronously on Zoom
 - Or provide asynchronous lectures
 - Maybe a combination, stay tuned

Review



Different IRs

Many different IRs, each have different purposes

- Trees
 - Abstract syntax trees
 - Data-dependency trees
 - Good for instruction scheduling
- Textual
 - 3 address code
 - Good for removing redundant expressions
- Graphs
 - Control flow graphs
 - Good for data flow analysis (finding uninitialized variables)

Abstract Syntax Trees

• Easier to see bigger trees, e.g. quadratic formula:

$$x = \frac{-b \pm \sqrt{b^2 - 4ac}}{2a}$$

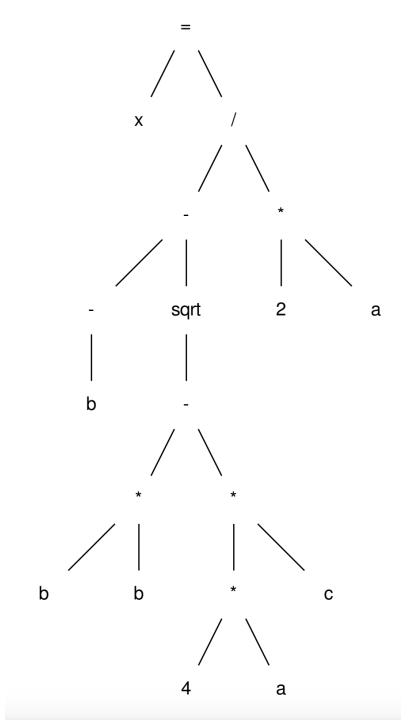
$$x = (-b - sqrt(b*b - 4 * a * c)) / (2*a)$$

Thanks to Sreepathi Pai for the example!

Convert this code to 3 address code

post-order traversal, creating virtual registers for each node

r0 = neg(b);r1 = b * b;r2 = 4 * a;r3 = r2 * c;r4 = r1 - r3;r5 = sqrt(r4);r6 = r0 - r5;r7 = 2 * a;r8 = r6 / r7; x = r8;



What about control flow?

• 3 address code typically contains a conditional branch:

br <reg>, <label0>, <label1>

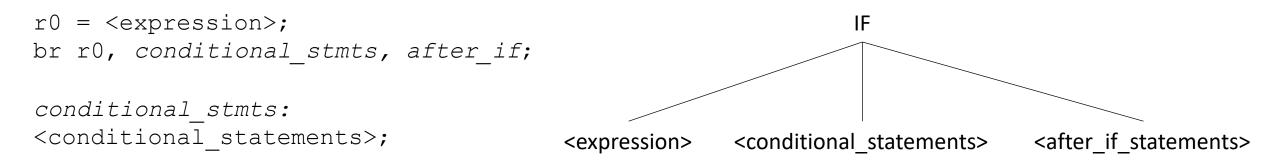
if the value in <reg> is true, branch to <label0>, else branch to <label1>

br <label0>

unconditional branch

What about control flow?

```
if (expr) {
   // conditional statements
}
// after if statements
```



```
after_if:
<after if statements>;
```

What about control flow?

```
while (expr) {
   // inside loop statements
  // after loop statements
                                                       WHILE
beginning label:
r0 = \langle expr \rangle
                                                <inside_loop_statements>
                                                                      <after_loop_statements>
                                      <expr>
br r0, inside loop, after loop;
inside loop:
<inside loop statements>
br beginning label;
after loop:
<after loop_statements>
```

New material

- A sequence of 3 address instructions
- Programs can be split into **Basic Blocks**:
 - A sequence of 3 address instructions such that:
 - There is a single entry, single exit

• *Important property*: an instruction in a basic block can assume that all preceding instructions will execute

Single Basic Block

- A sequence of 3 address instructions
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• *Important property*: an instruction in a basic block can assume that all preceding instructions will execute

Single Basic Block Label x: op1; op2; op3; br label z;

Two Basic Blocks

```
Label_x:
op1;
op2;
op3;
Label_y:
op4;
op5;
```

How might they appear in a high-level language? What are some examples?

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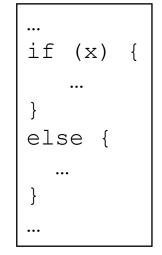
op4;

op5;

- A sequence of 3 address instructions
- Programs can be split into **Basic Blocks**:
 - A sequence of 3 address instructions such that:
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• *Important property*: an instruction in a basic block can assume that all preceding instructions will execute How might they appear in a high-level language?

Four Basic Blocks



Two Basic Blocks

 Single Basic Block
 Label_x:

 Label_x:
 op1;

 op1;
 op2;

 op3;
 Label_y:

 br label_z;
 op4;

 op5;

• Local optimizations:

• Optimizes an individual basic block

• Regional optimizations:

• Combines several basic blocks

Global optimizations:

- operates across an entire procedure
- what about across procedures?

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Label_0: x = a + b; y = a + b;

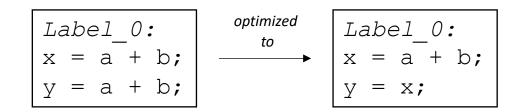
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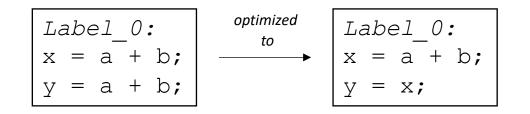
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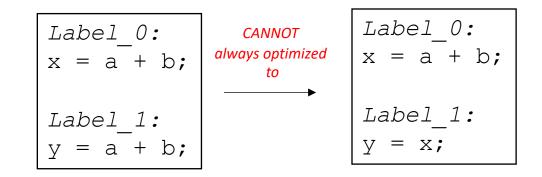


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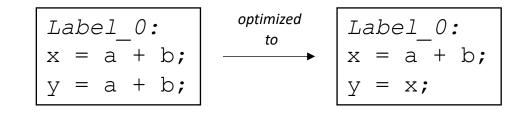
Global optimizations:

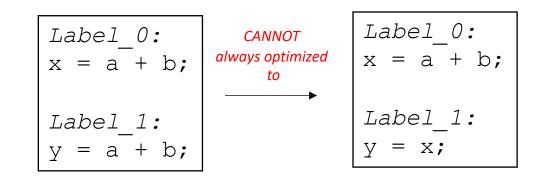
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- Global optimizations:
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code could skip Label_0, leaving x undefined!

Regional Optimization

… if (x) {	
 } else {	
x = a + b; }	
y = a + b; 	

at a higher-level, we cannot replace: y = a + b. with y = x;

Regional Optimization

 if	-	(x)		[
	•	••		-		a				
}		_	r			W				
l e l		e { =	•	+	b;					
}			-		- ,					
У	=	а	+	b	;					

at a higher-level, ve cannot replace: y = a + b. with y = x;

x = a + b; if (x) {	
	But
}	not r
else {	
	can b
}	
y = a + b;	

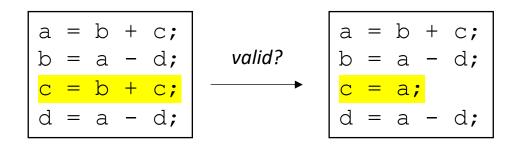
```
But if a and b are
not redefined, then
y = a + b;
can be replaced with
y = x;
```

Today's lecture: A local optimization

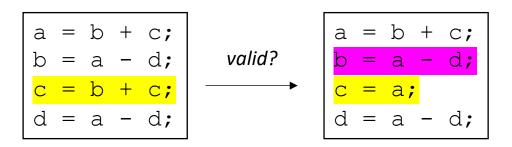
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- Attempts to replace arithmetic operations (expensive) with copy instructions (cheap)
- Can be extended to a regional optimization using flow analysis
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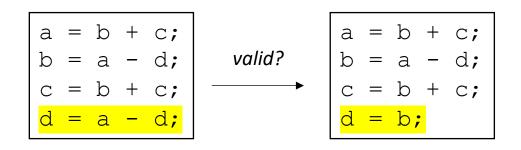


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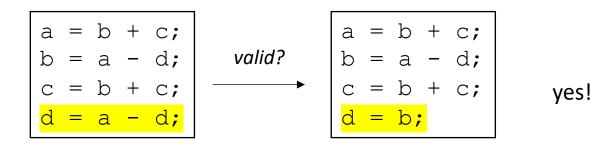


No! Because b is redefined

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Algorithm:

- Provide a number to each variable. Update the number each time the variable is updated.
- Keep a global counter; increment with new variables or assignments

Global_counter = 6

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Global_counter = 7

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.

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Algorithm: Now that variables are numbered

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
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to

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$$a2 = b0 + c1;b4 = a2 - d3;c5 = b4 + c1;b44 = 4;d6 = b4;$$

What else can we do?

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Consider this snippet:

Commutative operations

What is the definition of commutative?

Commutative operations

What is the definition of commutative?

What operators are commutative? Which ones are not?

Adding commutativity to local value numbering

- For commutative operators (e.g. + *), the analysis should consider a deterministic order of operands.
- You can use variable numbers or lexigraphical order

Algorithm optimization:

Algorithm optimization:

for commutative operations, re-order operands into a deterministic order

cannot re-order because - is not commutative

Algorithm optimization:

Algorithm optimization:

for commutative operations, re-order operands into a deterministic order

re-ordered because a2 < d3 lexigraphically

Algorithm optimization:

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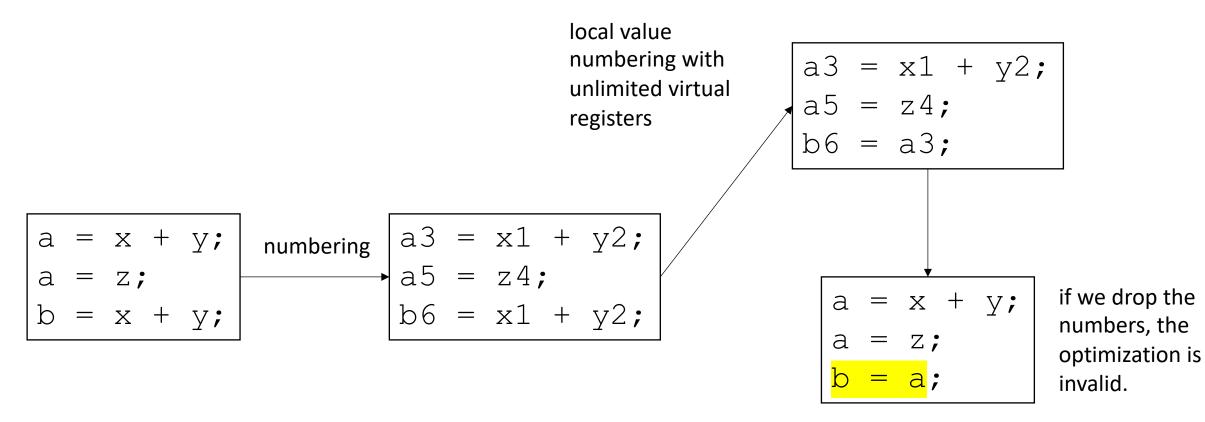
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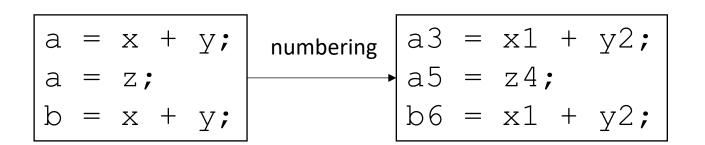
Other considerations?

- We've assumed we have access to an unlimited number of virtual registers.
- In some cases we may not be able to add virtual registers
 - If an expensive register allocation pass has already occurred.
- New constraint:
 - We need to produce a program such that variables without the numbers is still valid.

• Example:

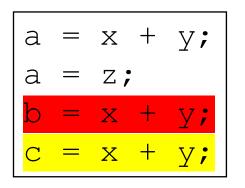


• Solutions?



a	=	Х	+	у;
a	=	Z ;		
b	=	Х	+	у;
С	=	Х	+	у;

• Keep another hash table to keep the current variable number



We cannot optimize the first line, but we can optimize the second

a	=	Х	+	у;
a	=	Z ;		
b	=	Х	+	у;
С	=	Х	+	у;

a	=	Х	+	у;
a	=	Z ;		
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С	=	Х	+	у;

Current_val = {
"a" : 5,
}

$$A3 = x1 + y2;$$

 $a5 = z4;$
 $b6 = x1 + y2;$
 $c7 = x1 + y2;$
 $Current_val = {
"x1 + y2" : "a3",
}$

Local value numbering w/out adding registers

"<mark>b6</mark>",

• Keep another hash table to keep the current variable number

Local value numbering w/out adding registers

: 6

• Keep another hash table to keep the current variable number

Anything else we can add to local value numbering?

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• Final heuristic: keep sets of possible values

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Current_val = {
}

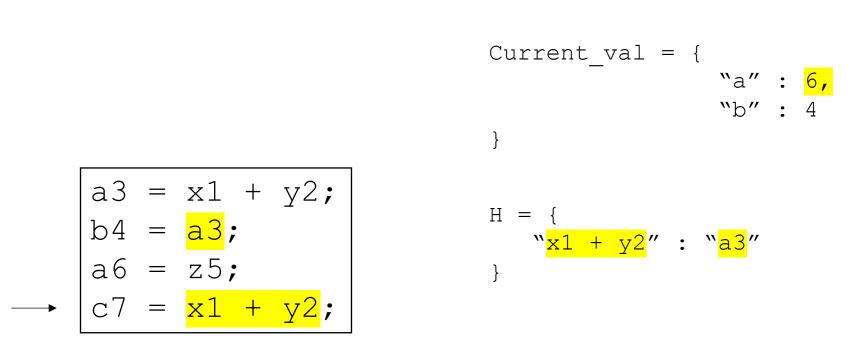
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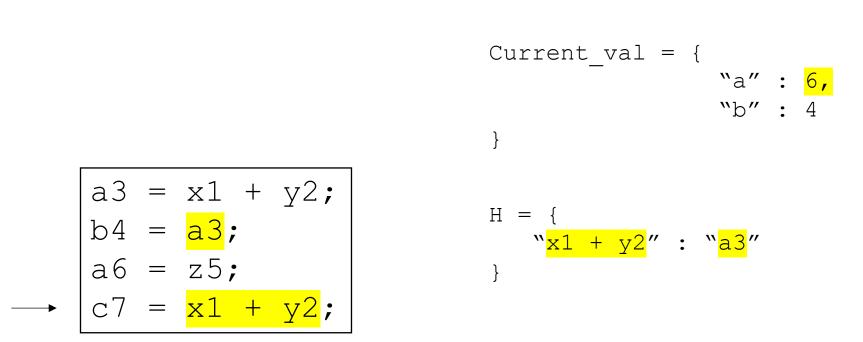
Current_val = {
}

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• Final heuristic: keep sets of possible values



but we could have replaced it with b4!

• Final heuristic: keep sets of possible values

rewind to this point

 $\begin{array}{c} a3 = x1 + y2; \\ b4 = x1 + y2; \\ a6 = z5; \\ c7 = x1 + y2; \end{array}$

• Final heuristic: keep sets of possible values

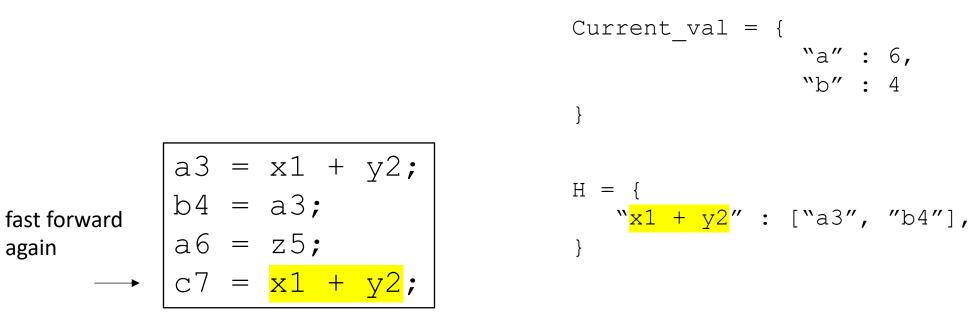
Current_val = {
"a" : 3,
"b" : 4
}

$$A3 = x1 + y2;$$

 $b4 = a3;$
 $a6 = z5;$
 $c7 = x1 + y2;$
H = {
"x1 + y2" : ["a3", "b4"],
hash a list of possible values

• Final heuristic: keep sets of possible values

again



• Final heuristic: keep sets of possible values

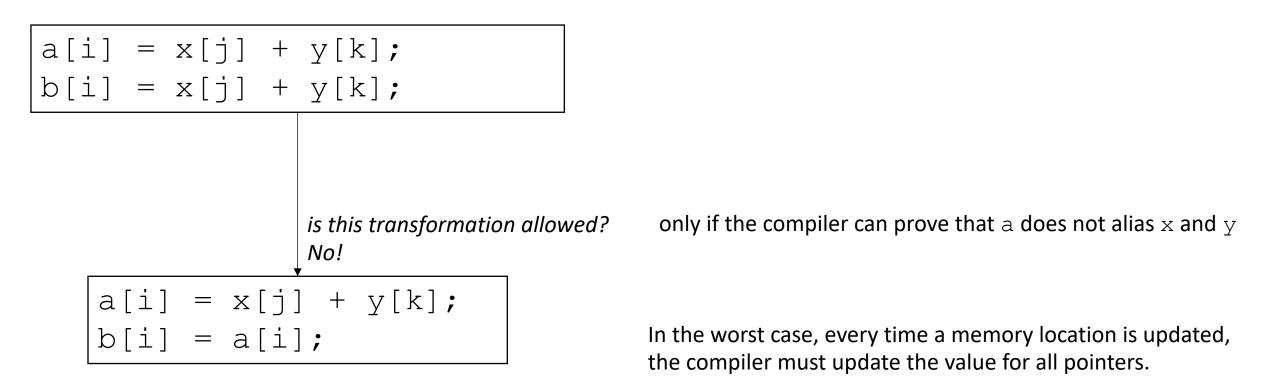
again

fast forward
again

$$\rightarrow$$
 $a3 = x1 + y2;$
 $b4 = a3;$
 $a6 = z5;$
 $c7 = b4;$
 $Current_val = {
"a" : 6,
"b" : 4
}
H = {
"x1 + y2" : ["a3", "b4"]
}$

1

Consider a 3 address code that allows memory accesses



- How to number:
 - Number each pointer/index pair

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 - Any pointer/index pair that might alias must be incremented at each instruction

(a[i],3) = (x[j],1) + (y[k],2); (b[i],6) = (x[j],4) + (y[k],5);

- How to number:
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compiler analysis:

can we trace a, x, y to
a = malloc(...);
x = malloc(...);
y = malloc(...);

// a, x, y are never overwritten

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restrict a

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(a[i],3) = (x[j],1) + (y[k],2); (b[i],6) = (a[i],3);

Optimizing over wider regions

- Local value numbering operated over just one basic block.
- We want optimizations that operate over several basic blocks (a region), or across an entire procedure (global)
- For this, we need Control Flow Graphs and Flow Analysis