CSE110A: Compilers

May 16, 2022

Topics:

- Basic blocks
- Local value numbering

Announcements

- New grades:
 - HW 2 posted
 - Please let us know within 1 week if there are any issues!
- Pending grades
 - Midterm (expect by next Friday)
- HW 3 is released
 - Due in two weeks from release date
 - Get started early; you have all the material you need!
 - Packet updated (hopefully for the last time). Just updated the path to classir.h in ir_compiler.py.
 - Keep your eye on piazza for this assignment!

Announcements

- HW 4 should be released by May 23
 - This will give you 2 weeks to get it in before the final date (June 7)
 - You cannot turn this homework in after June 7
 - This is not my policy, it is the department policy!

Quiz

Quiz

int x = 1 + 2;
int y = 1 + x * x * x;
int $z = x + y * 1 + 2 + 3;$
<i>if</i> (<i>z</i> == 2+ y * 1) {
int $w = 1 + 2 + 3;$
}
○ 1 + 2 + 3
○ x * x * x
○ y * 1 + 2
○ 2 + 3

Discussion

○ 1 + 2 + 3

 $\bigcirc x * x * x$

○ y * 1 + 2

O 2 + 3

int x = 1 + 2; int y = 1 + x * x * x; int z = x + y * 1 + 2 + 3; if (z == 2+ y * 1) { int w = 1 + 2 + 3; }

Quiz

Perform Constant propagation on the following program; what would the function return? (assume `if-statement` is a 'constexpr if-statement')

```
int a = 30;
int b = 9 - (a / 5);
int c;
c = b * 4;
if (c > 10) {
    c = c - 10;
}
return c * (60 / a);
```

Discussion

int a = 30; int b = 9 - (a / 5); int c; c = b * 4; if (c > 10) { c = c - 10; } return c * (60 / a);

Quiz

loop unrolling is a _____ optimization

 \bigcirc local

 \bigcirc regional

 \bigcirc global

Optimization categories

- **local optimizations**: examine a "basic block", i.e. a small region of code with no control flow.
- Regional optimizations: several basic blocks with simple control flow.
- Global optimization: optimizes across an entire function



Optimization categories

- local optimizations: examine a "basic block", i.e. a small region of code with no control flow.
- **Regional optimizations**: several basic blocks with simple control flow
- Global optimization: optimizes across an entire function

Discussion:

- What are the pros and cons of each?
- Why don't we go further than functions?

Basic blocks

- A sequence of 3 address instructions
- Programs can be split into **Basic Blocks**:
 - A sequence of 3 address instructions such that:
 - There is a single entry, single exit

• *Important property*: an instruction in a basic block can assume that all preceding instructions will execute Single Basic Block

Label x: op1; op2; op3; br label z;

- A sequence of 3 address instructions
- Programs can be split into **Basic Blocks**:
 - A sequence of 3 address instructions such that:
 - There is a single entry, single exit

• *Important property*: an instruction in a basic block can assume that all preceding instructions will execute

Single Basic Block Label x: op1; op2; op3; br label z;

Two Basic Blocks

```
Label_x:
op1;
op2;
op3;
Label_y:
op4;
op5;
```

How might they appear in a high-level language? What are some examples?

- A sequence of 3 address instructions
- Programs can be split into **Basic Blocks**:
 - A sequence of 3 address instructions such that:
 - There is a single entry, single exit

• *Important property*: an instruction in a basic block can assume that all preceding instructions will execute

Single Basic Block Label x: op1; op2; op3; br label z;

Two Basic Blocks

Label x:

Label y:

op1;

op2;

op3;

op4;

op5;

- A sequence of 3 address instructions
- Programs can be split into **Basic Blocks**:
 - A sequence of 3 address instructions such that:
 - There is a single entry, single exit

• *Important property*: an instruction in a basic block can assume that all preceding instructions will execute How might they appear in a high-level language?

How many basic blocks?



Two Basic Blocks

Single Basic BlockLabel_x:
op1;
op2;
op3;Image: Description of the second structureImage: Descr

Converting 3 address code into basic blocks

• Let's try an example: test 4 in HW 3:

Converting 3 address code into basic blocks

- Simple algorithm:
 - keep a list of basic blocks
 - a basic block is a list of instructions
 - Iterate over the 3 address instructions
 - if you see a branch or a label, finalize the current basic block and start a new one.
 - otherwise just add the current instruction to the current basic block

Converting 3 address code into basic blocks

pseudo code

```
basic_blocks = []
bb = []
for instr in program:
    if instr type is in [branch, label]:
        bb.append(instr)
        basic_blocks.append[bb]
        bb = []
    else:
        bb.append(instr)
```

• Local optimizations:

• Optimizes an individual basic block

• Regional optimizations:

• Combines several basic blocks

• Global optimizations:

• operates across an entire procedure

- Local optimizations:
 - Optimizes an individual basic block

• Regional optimizations:

• Combines several basic blocks

• Global optimizations:

• operates across an entire procedure

Label_0: x = a + b; y = a + b;

- Local optimizations:
 - Optimizes an individual basic block

• Regional optimizations:

Combines several basic blocks

• Global optimizations:

• operates across an entire procedure



- Local optimizations:
 - Optimizes an individual basic block
- Regional optimizations:
 - Combines several basic blocks
- Global optimizations:
 - operates across an entire procedure





- Local optimizations:
 - Optimizes an individual basic block
- Regional optimizations:
 - Combines several basic blocks
- Global optimizations:
 - operates across an entire procedure





code could skip Label_0, leaving x undefined!

Regional Optimization

… if (x) {	
 } else {	١
x - a + b; y = a + b;	
•••	

we cannot replace: y = a + b. with y = x;

Regional Optimization

… if (x) {	
 } else {	W
x = a + b; }	
y = a + b; 	

y = a + b. with y = x;

This requires regional analysis

x = a + b;
if (x) {
•••
}
else {
•••
}
y = a + b;

But in this case, we can check if a and b are not redefined, then y = a + b;can be replaced with y = x;

- A local optimization over 3 address code
- Attempts to replace arithmetic operations (expensive) with copy instructions (cheap)
- Can be extended to a regional optimization using flow analysis

- A local optimization over 3 address code
- Attempts to replace arithmetic operations (expensive) with copy instructions (cheap)
- Can be extended to a regional optimization using flow analysis

$$a = b + c;$$

 $b = a - d;$
 $c = b + c;$
 $d = a - d;$

- A local optimization over 3 address code
- Attempts to replace arithmetic operations (expensive) with copy instructions (cheap)
- Can be extended to a regional optimization using flow analysis



- A local optimization over 3 address code
- Attempts to replace arithmetic operations (expensive) with copy instructions (cheap)
- Can be extended to a regional optimization using flow analysis



No! Because b is redefined

- A local optimization over 3 address code
- Attempts to replace arithmetic operations (expensive) with copy instructions (cheap)
- Can be extended to a regional optimization using flow analysis



- A local optimization over 3 address code
- Attempts to replace arithmetic operations (expensive) with copy instructions (cheap)
- Can be extended to a regional optimization using flow analysis



Algorithm:

- Provide a number to each variable. Update the number each time the variable is updated.
- Keep a global counter; increment with new variables or assignments

Global_counter = 0

Algorithm:

- Provide a number to each variable. Update the number each time the variable is updated.
- Keep a global counter; increment with new variables or assignments

Global_counter = 7

Algorithm: Now that variables are numbered

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.
- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.

$$\Rightarrow \begin{bmatrix} a2 &= b0 + c1; \\ b4 &= a2 - d3; \\ c5 &= b4 + c1; \\ d6 &= a2 - d3; \end{bmatrix}$$

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.

$$a2 = b0 + c1;$$

$$b4 = a2 - d3;$$

$$c5 = b4 + c1;$$

$$d6 = a2 - d3;$$

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.

$$a2 = b0 + c1;$$

$$b4 = a2 - d3;$$

$$c5 = b4 + c1;$$

$$d6 = a2 - d3;$$

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.

$$a2 = b0 + c1;$$

$$b4 = a2 - d3;$$

$$c5 = b4 + c1;$$

$$d6 = a2 - d3;$$

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.

- Iterate sequentially through instructions. Keep a hash table of the rhs (numbered variables and operation) mapped to their lhs.
- At each step, check to see if the rhs has already been computed.

What else can we do?

What else can we do?

Consider this snippet:

a2	=	c1	_	b0;
f4	=	d3	*	a2;
c5	=	b0	-	c1;
d6	=	a2	*	d3;

Commutative operations

What is the definition of commutative?

Commutative operations

What is the definition of commutative?

$$x OP y == y OP x$$

What operators are commutative? Which ones are not?

Adding commutativity to local value numbering

- For commutative operators (e.g. + *), the analysis should consider a deterministic order of operands.
- You can use variable numbers or lexigraphical order

Algorithm optimization:

Algorithm optimization:

for commutative operations, re-order operands into a deterministic order

cannot re-order because - is not commutative

Algorithm optimization:

Algorithm optimization:

for commutative operations, re-order operands into a deterministic order

re-ordered because a2 < d3 lexigraphically

a2	=	c 1	_	b0;
 f4	=	d3	*	a2;
c 5	=	b0	_	c1;
d6	=	a2	*	d3;

Algorithm optimization:

Algorithm optimization:

$$a2 = c1 - b0;$$

$$f4 = d3 * a2;$$

$$c5 = b0 - c1;$$

$$d6 = a2 * d3;$$

Algorithm optimization:

$$a2 = c1 - b0;$$

$$f4 = d3 * a2;$$

$$c5 = b0 - c1;$$

$$d6 = a2 * d3;$$

Algorithm optimization:

$$a2 = c1 - b0;$$

$$f4 = d3 * a2;$$

$$c5 = b0 - c1;$$

$$d6 = f4;$$

Other considerations?

- We've assumed we have access to an unlimited number of virtual registers.
- In some cases we may not be able to add virtual registers
 - If an expensive register allocation pass has already occurred.
- New constraint:
 - We need to produce a program such that variables without the numbers is still valid.

• Example:



• Solutions?



a	=	x	+	у;
a	=	Z	•	
b	=	Х	+	у;
С	=	Х	+	у;

• Keep another hash table to keep the current variable number



We cannot optimize the first line, but we can optimize the second

a	=	x	+	у;
a	=	Z	•	
b	=	Х	+	у;
С	=	Х	+	у;

a	=	X	+	у;
a	=	Z ;		
b	=	X	+	у;
С	=	X	+	У;

• Keep another hash table to keep the current variable number

 \rightarrow | a3 = x1 + y2; a5 = z4;

b6 = x1 + y2;c7 = x1 + y2;

• Keep another hash table to keep the current variable number

→ a3 = x1 + y2; a5 = z4; b6 = x1 + y2; c7 = x1 + y2;

Current_val = {
"a" : 5,
}

$$A3 = x1 + y2;$$

 $a5 = z4;$
 $b6 = x1 + y2;$
 $c7 = x1 + y2;$
 $Current_val = {
"x1 + y2" : "a3",
}$

• Keep another hash table to keep the current variable number

a3 = x1 + y2;

 $\Rightarrow \begin{vmatrix} a5 &= z4; \\ b6 &= x1 + y2; \\ c7 &= x1 + y2; \end{vmatrix}$
• Keep another hash table to keep the current variable number

"b6",

• Keep another hash table to keep the current variable number

• Keep another hash table to keep the current variable number

a3

• Keep another hash table to keep the current variable number

 \longrightarrow

a3

a5

b6

• Keep another hash table to keep the current variable number

——**)**

a3

a5

b6

Anything else we can add to local value numbering?

Anything else we can add to local value numbering?

• Final heuristic: keep sets of possible values

Current_val = {
}

a	=	x	+	у;
b	=	Х	+	у;
а	=	Z ;		
С	=	X	+	У;



• Final heuristic: keep sets of possible values



but we could have replaced it with b4!

• Final heuristic: keep sets of possible values

rewind to this point a3 = x1 + y2; b4 = x1 + y2; a6 = z5; c7 = x1 + y2;

• Final heuristic: keep sets of possible values

possible values

• Final heuristic: keep sets of possible values



fast forward again

• Final heuristic: keep sets of possible values

again

fast forward
again

$$\rightarrow$$
 $a3 = x1 + y2;$
 $b4 = a3;$
 $a6 = z5;$
 $c7 = b4;$
 $Current_val = {
"a" : 6,
"b" : 4
}
H = {
"x1 + y2" : ["a3", "b4"]
}$

1

Consider a 3 address code that allows memory accesses



- How to number:
 - Number each pointer/index pair

- How to number:
 - Number each pointer/index pair
 - Any pointer/index pair that might alias must be incremented at each instruction

(a[i],3) = (x[j],1) + (y[k],2);(b[i],6) = (x[j],4) + (y[k],5);

- How to number:
 - Number each pointer/index pair
 - Any pointer/index pair that might alias must be incremented at each instruction

$$(a[i],3) = (x[j],1) + (y[k],2);$$

 $(b[i],6) = (x[j],4) + (y[k],5);$

compiler analysis:

can we trace a, x, y to
a = malloc(...);
x = malloc(...);
y = malloc(...);

// a, x, y are never overwritten

- How to number:
 - Number each pointer/index pair
 - Any pointer/index pair that might alias must be incremented at each instruction

in this case we do not have to update the number

compiler analysis:

can we trace a, x, y to
a = malloc(...);
x = malloc(...);
y = malloc(...);

// a, x, y are never overwritten

- How to number:
 - Number each pointer/index pair
 - Any pointer/index pair that might alias must be incremented at each instruction

$$(a[i],3) = (x[j],1) + (y[k],2);$$

 $(b[i],6) = (x[j],4) + (y[k],5);$

programmer annotations can also tell the compiler that no other pointer can access the memory pointed to by a

- How to number:
 - Number each pointer/index pair
 - Any pointer/index pair that might alias must be incremented at each instruction

$$(a[i],3) = (x[j],1) + (y[k],2);$$

 $(b[i],6) = (x[j],4) + (y[k],5);$

in this case we do not have to update the number

restrict a

programmer annotations can also tell the compiler that no other pointer can access the memory pointed to by a

- How to number:
 - Number each pointer/index pair
 - Any pointer/index pair that might alias must be incremented at each instruction

(a[i],3) = (x[j],1) + (y[k],2); (b[i],6) = (a[i],3);

Optimizing over wider regions

- Local value numbering operated over just one basic block.
- We want optimizations that operate over several basic blocks (a region), or across an entire procedure (global)
- For this, we need Control Flow Graphs and Flow Analysis
 - We may have time to discuss this later in the module

See everyone on Wednesday

• Topics: Loop unrolling